

Form PTO-1449

INFORMATION DISCLOSURE CITATION
IN AN APPLICATION.

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Document Number (Continuation)

TSMC-00-0688D

Assignment Number

10/790,919

Applicant

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Filing Date

03/02/04

Group Art Unit

U. S. PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	TITLE	CLASS	SUBCLASS	FILED DATE & APPROPRIATE
TD 5850095	12/15/98	Chen et al.	257	361	9/24/96
TD 5341005	8/23/94	Canclini	257	173	9/8/92
TD 5528189	6/18/96	Khatibzadeh	327	314	12/21/93
TD 5301084	4/5/94	Miller	361	91	8/21/91

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation
					YES NO

OTHER DOCUMENTS (Including Author, Title, Date, Portion of Pages, Etc.)

TD	J. Chen et al., "Design and Layout of a High ESD Performance NPN Structure for Submicron BiCMOS/Bipolar Circuits", IEEE Jnl 1996, 0-7803-2753- 5/96, pp. 227-232.

EXAMINER

2/Jan

DATE COMPLETED

07/21/04

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